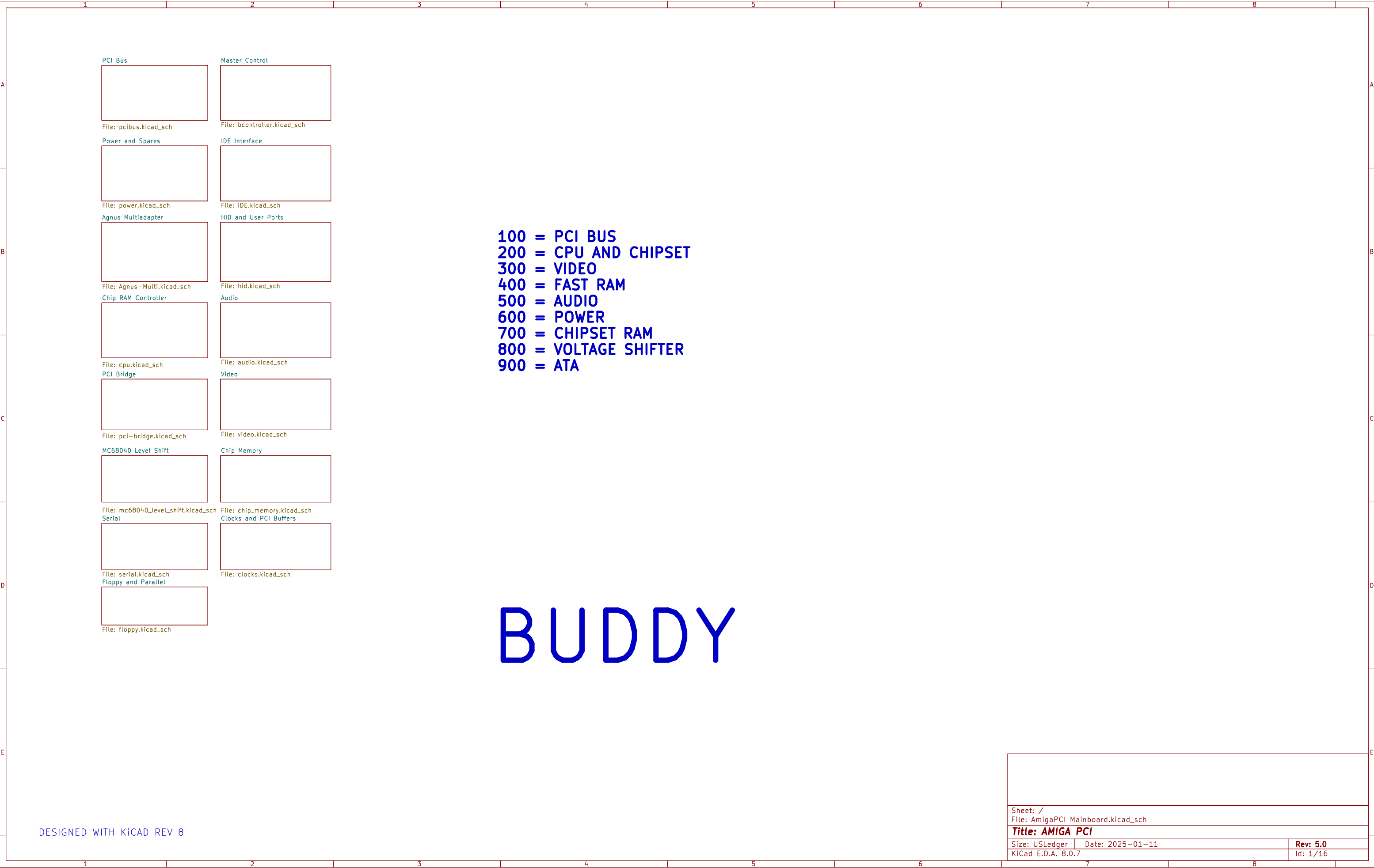




100 = PCI BUS

200 = CPU AND CHIPSET

300 = VIDEO

400 = FAST RAM

500 = AUDIO

600 = POWER

700 = CHIPSET RAM

800 = VOLTAGE SHIFTER

900 = ATA

BUDDY

DESIGNED WITH KICAD REV 8

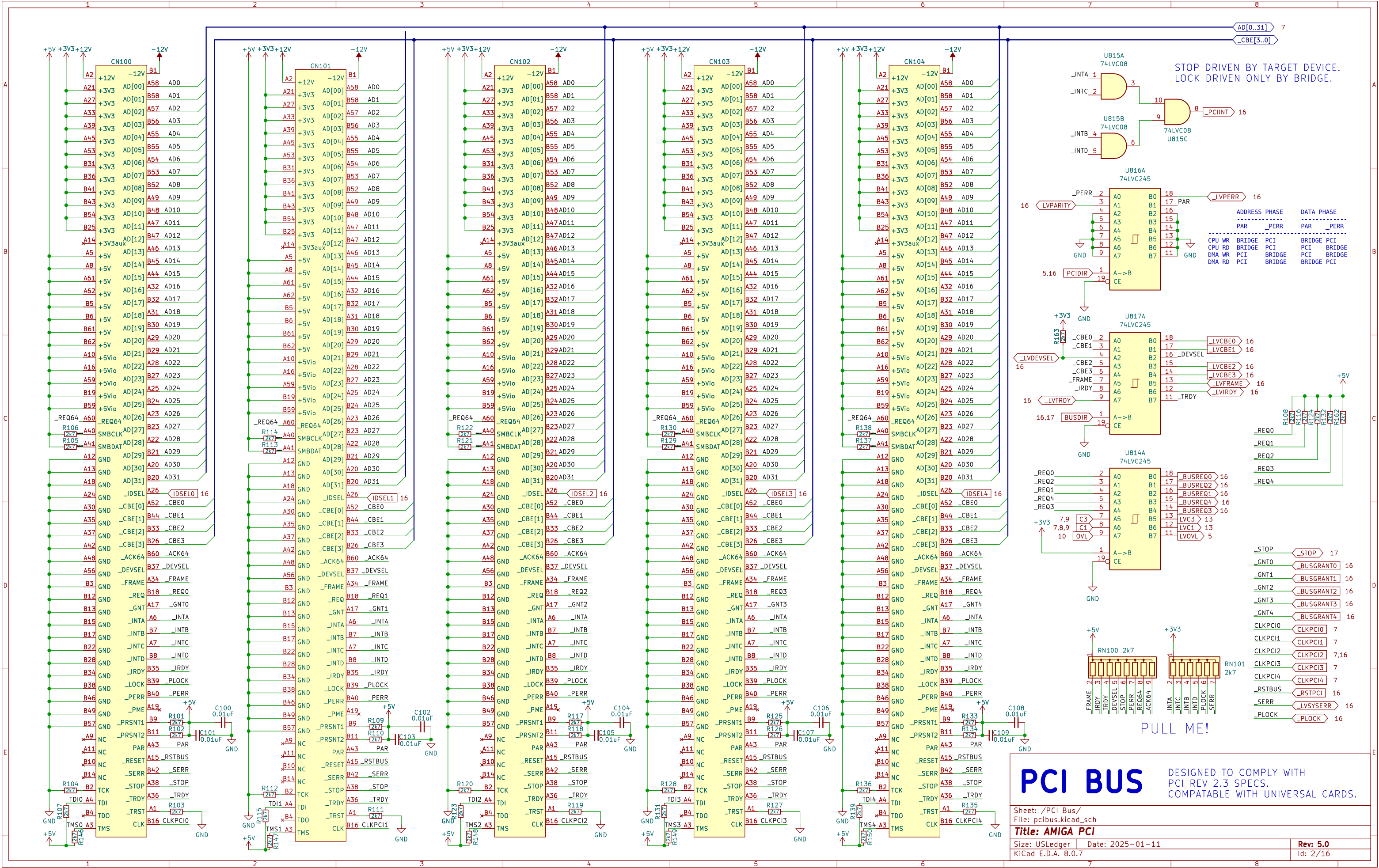
Sheet: /

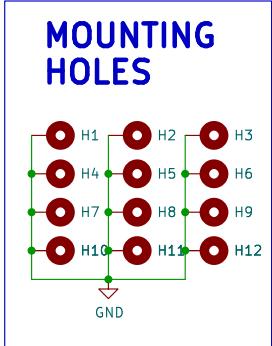
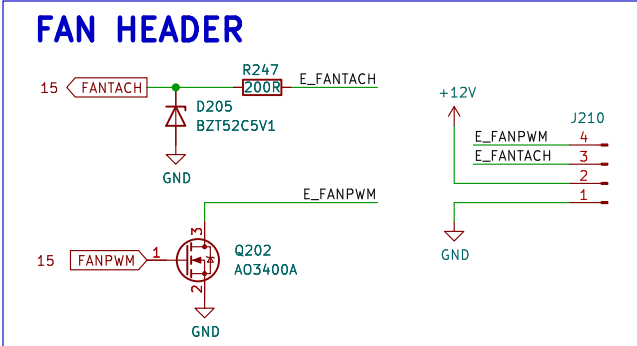
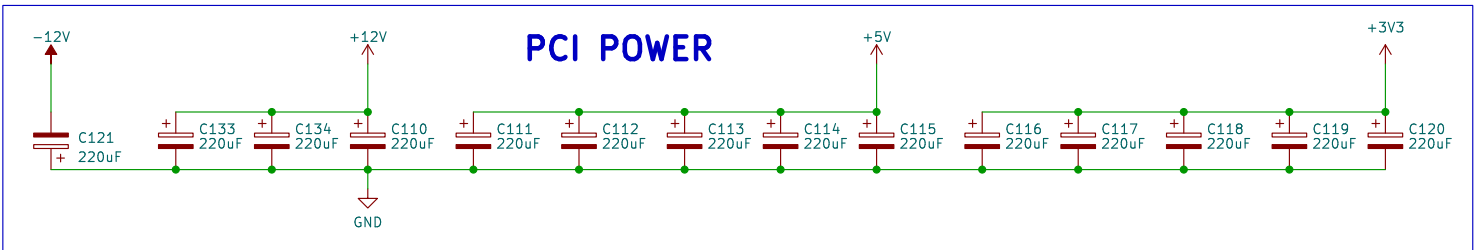
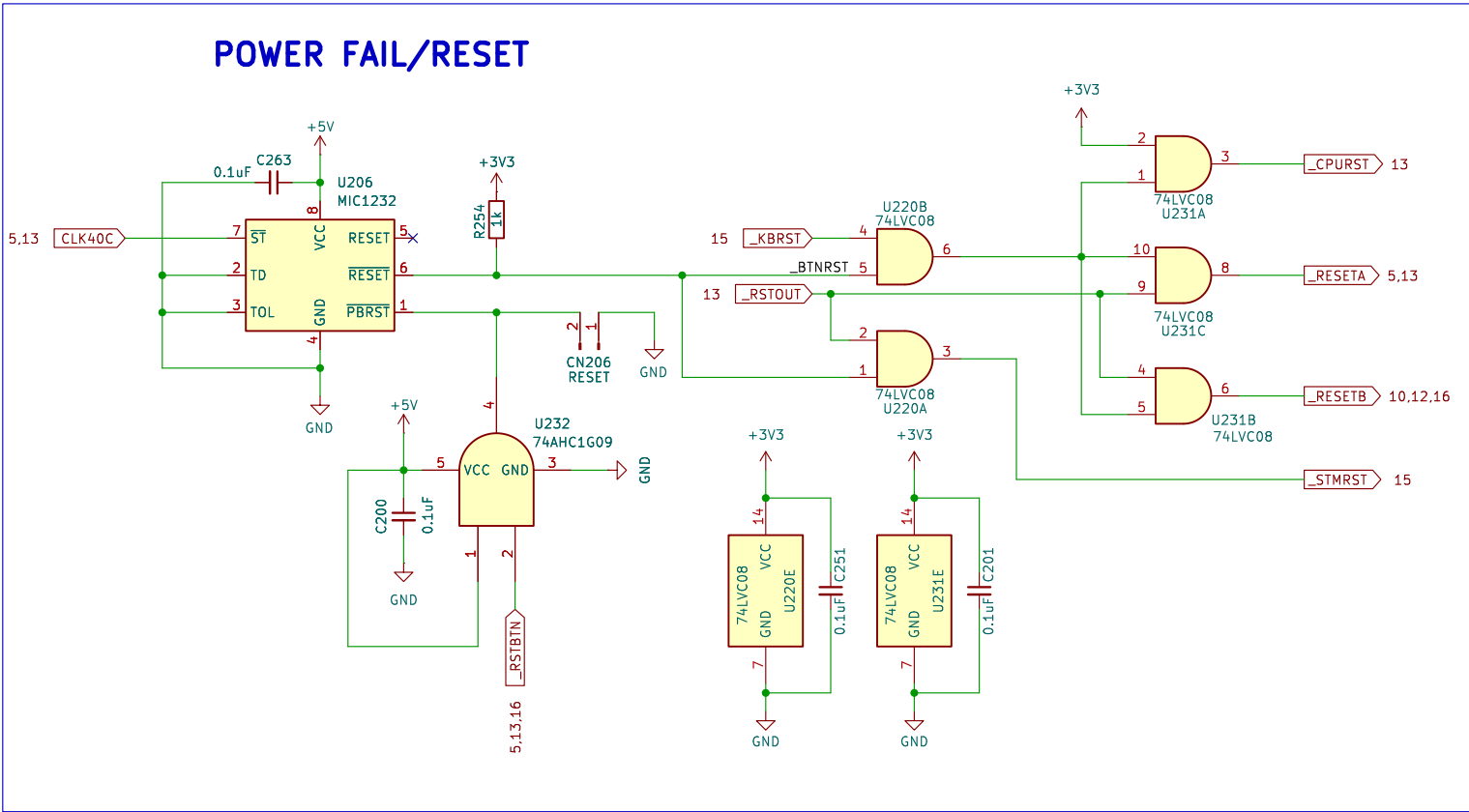
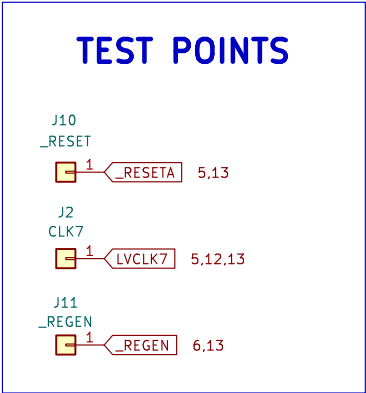
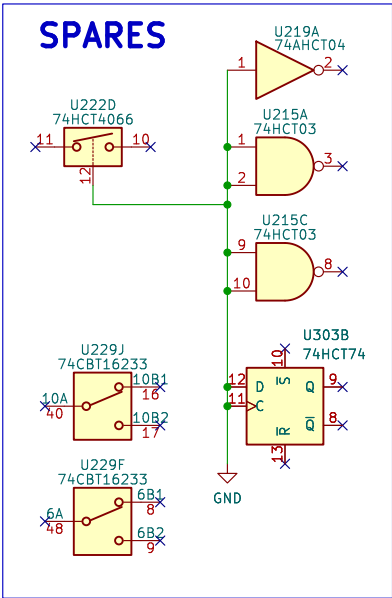
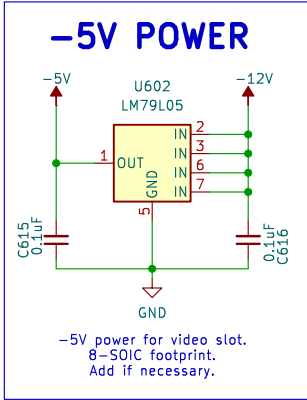
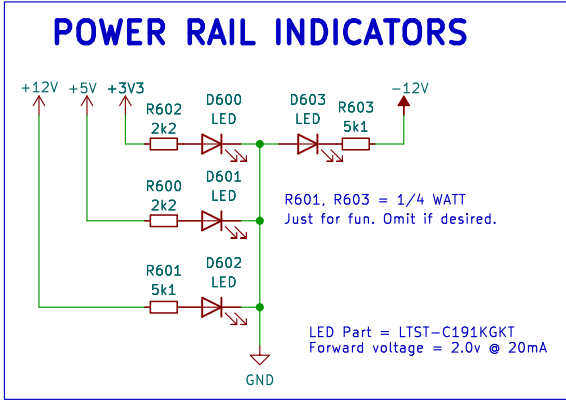
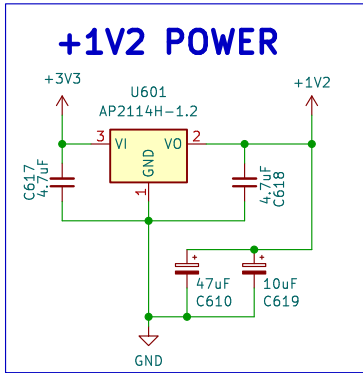
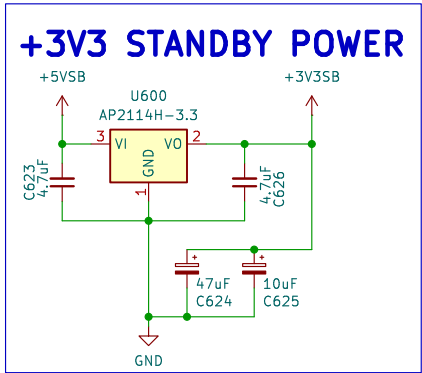
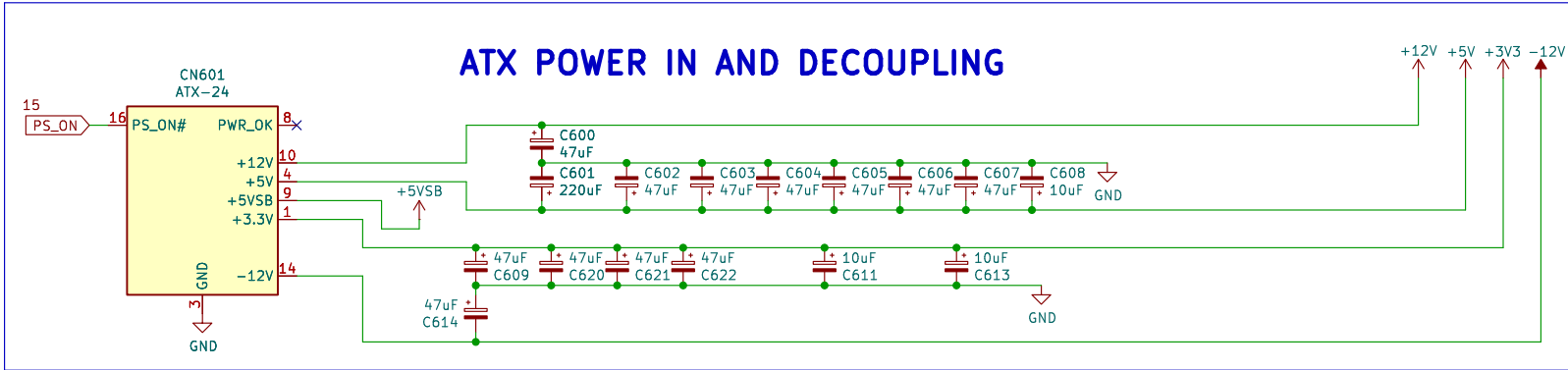
File: AmigaPCI Mainboard.kicad_sch

Title: AMIGA PCI

Size: USLedger Date: 2025-01-11 Rev: 5.0

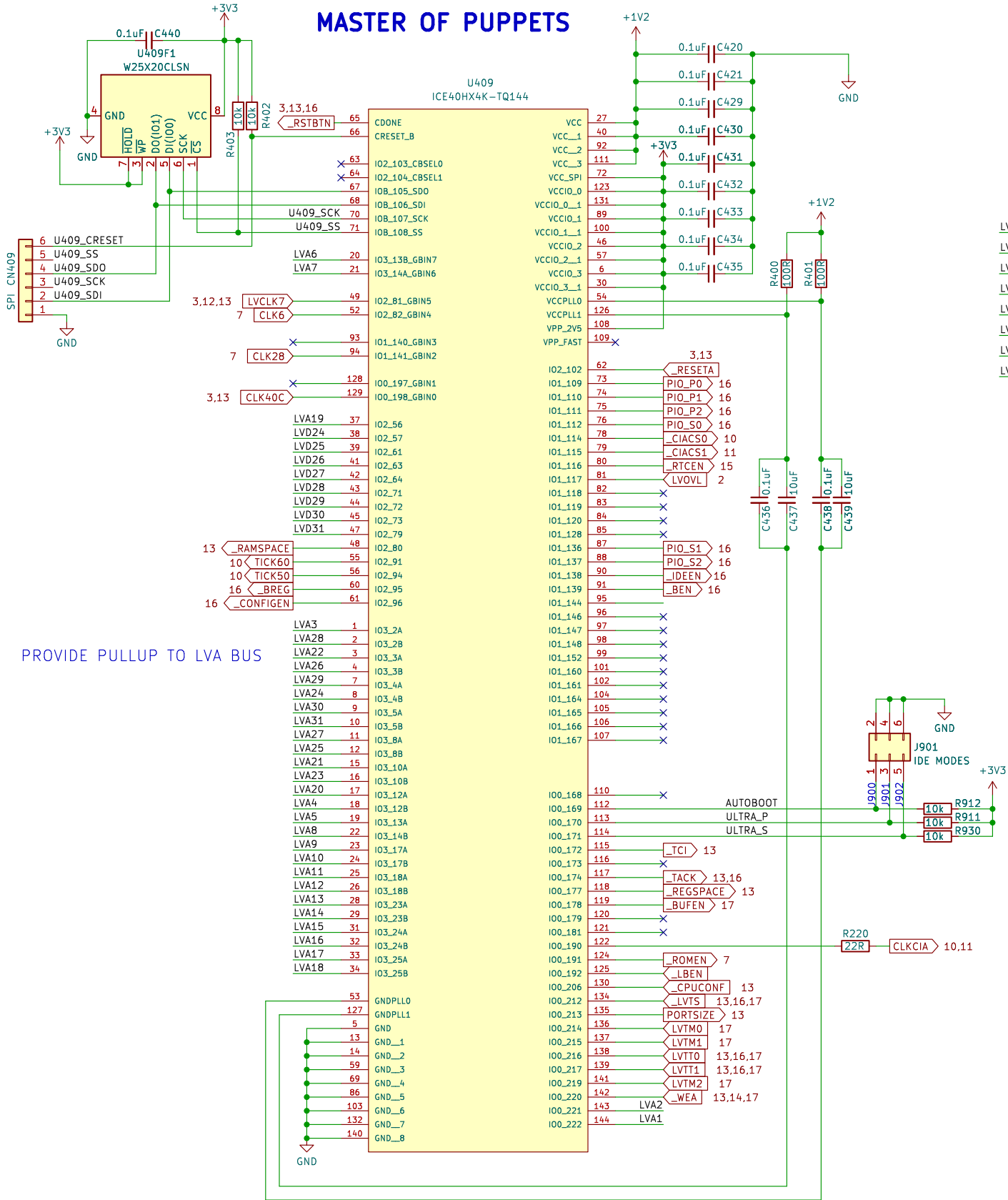
KiCad E.D.A. 8.0.7 Id: 1/16



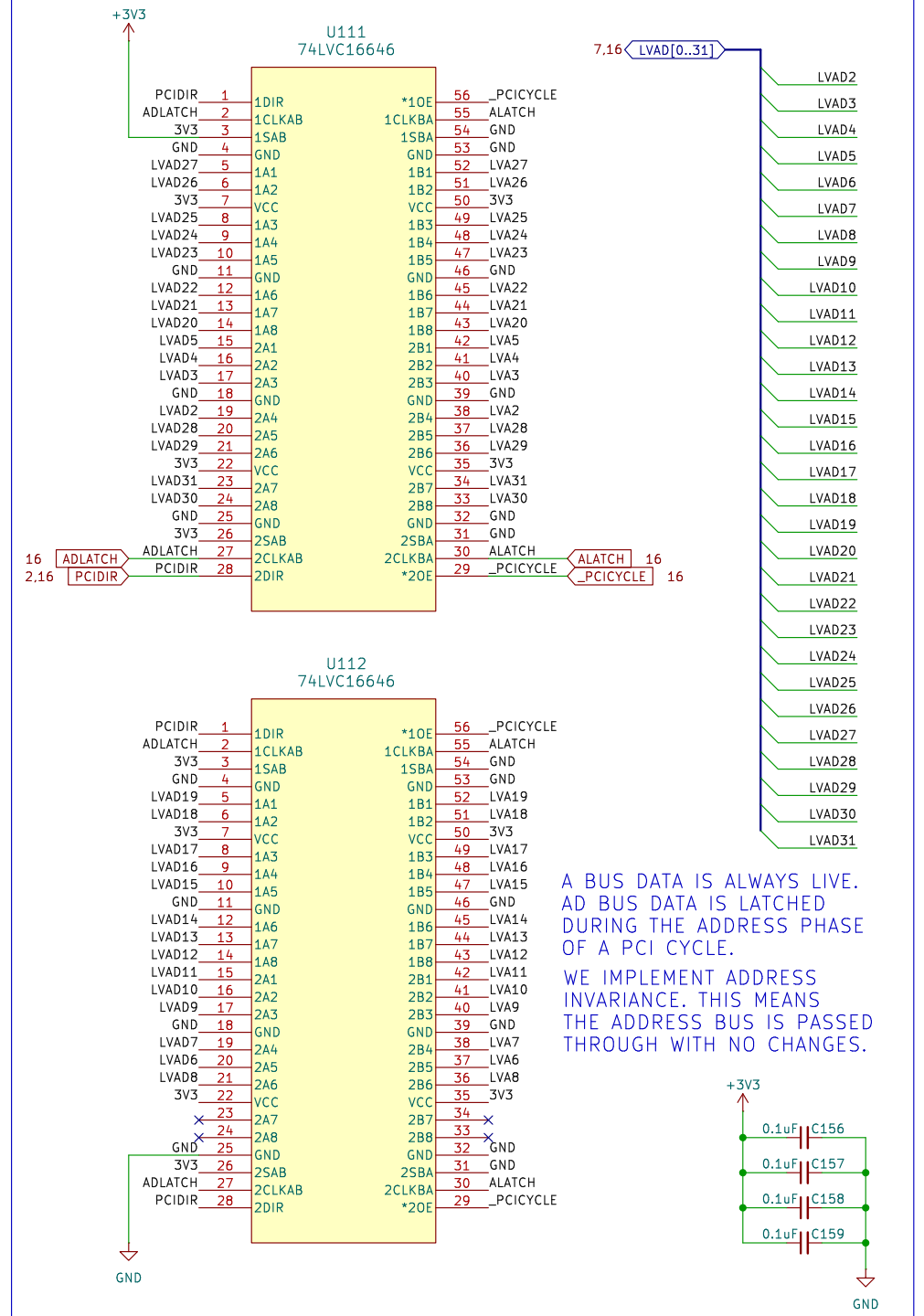


POWER/SPARES

MASTER OF PUPPETS



PCI ADDRESS LATCH



U409 PROVIDES ONBOARD DEVICE AUTOCONFIG, ADDRESS DECODE, AND REPLACEMENT GARY FUNCTIONS

MASTER CONTROL

Sheet: /Master Control/
File: bcontroller.kicad_sch

Title: AMIGA PCI

Size: B Date: 2025-01-11

KiCad E.D.A. 8.0.7

Rev: 5.0

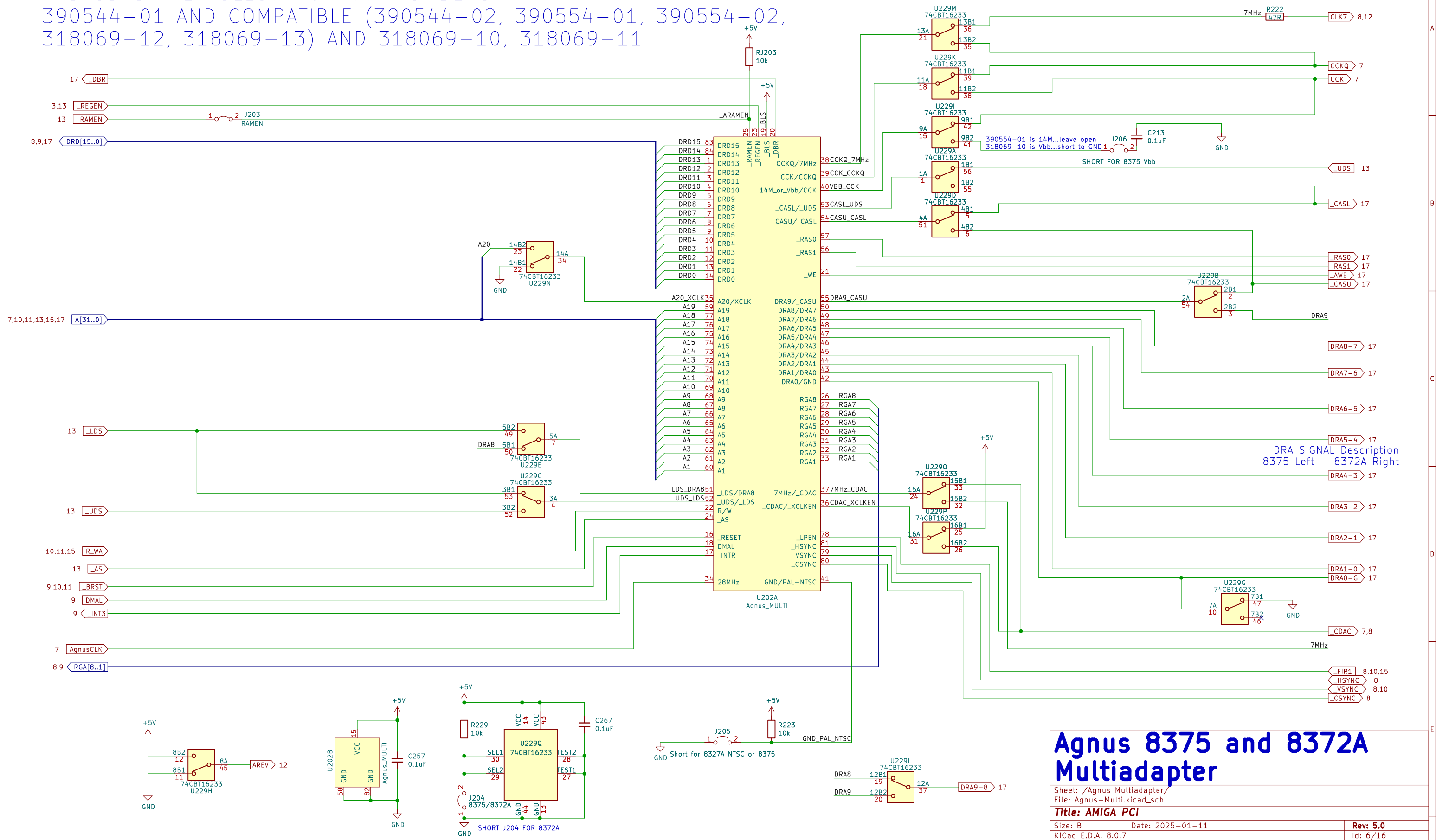
Id: 5/16

SUPPORTS 8372A (ALL PART NUMBERS) AND 8375 THE FOLLOWING PART NUMBERS:
390544-01 AND COMPATIBLE (390544-02, 390554-01, 390554-02, 318069-12, 318069-13) AND 318069-10, 318069-11

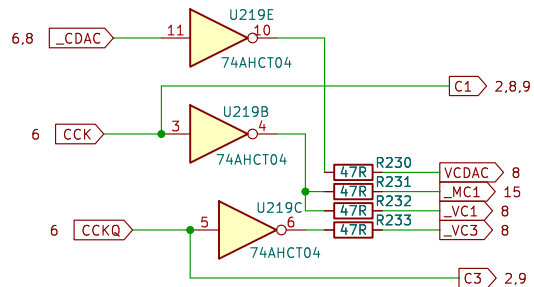
JUMPERS MUST BE CONFIGURED BY AGNUS PART NUMBER



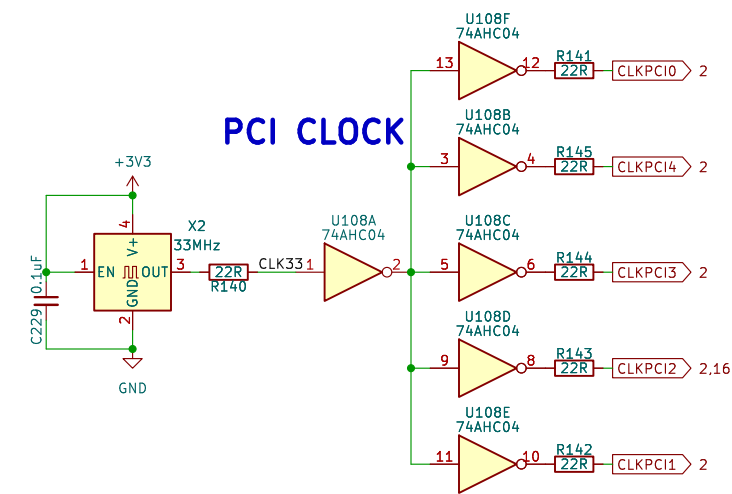
Diagram illustrating the connection of the 74CBT16233 (U229M) to the 7MHz oscillator (R222) and the CLK7 input of the 8372A/8375. The 74CBT16233 is a 3-state buffer with pins 13A, 21, 13B1, 36, 13B2, 35, and 34. The 7MHz oscillator is connected to pin 13B1. The output of the oscillator is connected to pin 13B2. The output of the oscillator is also connected to the CLK7 input of the 8372A/8375.



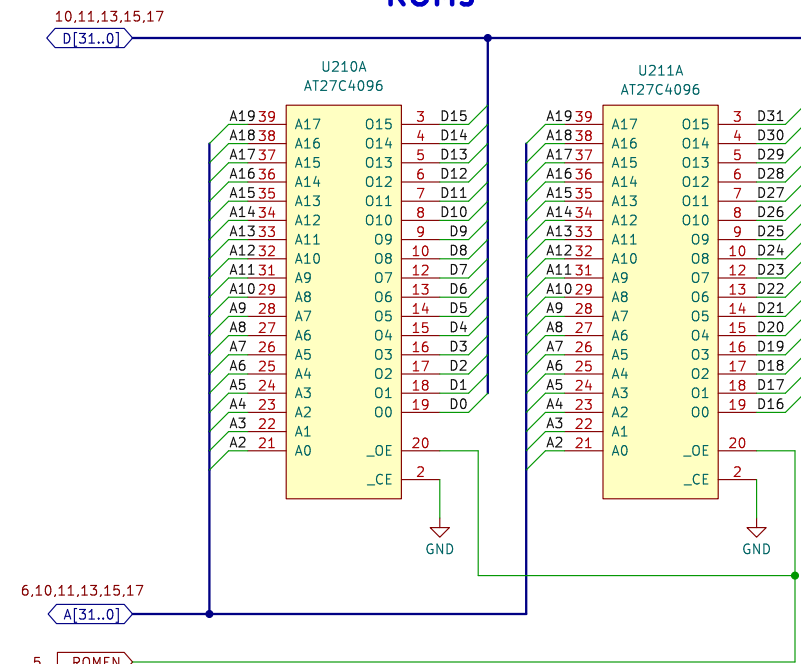
VIDEO CLOCK FANOUT



PCI CLOCK

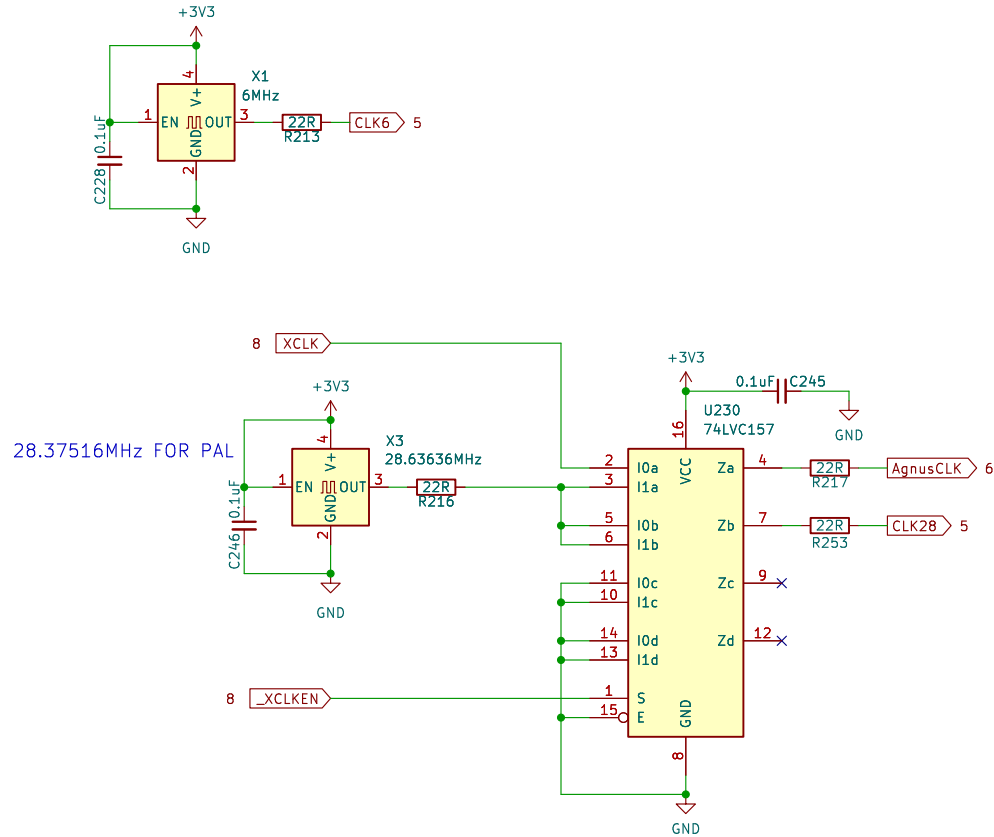


ROMs

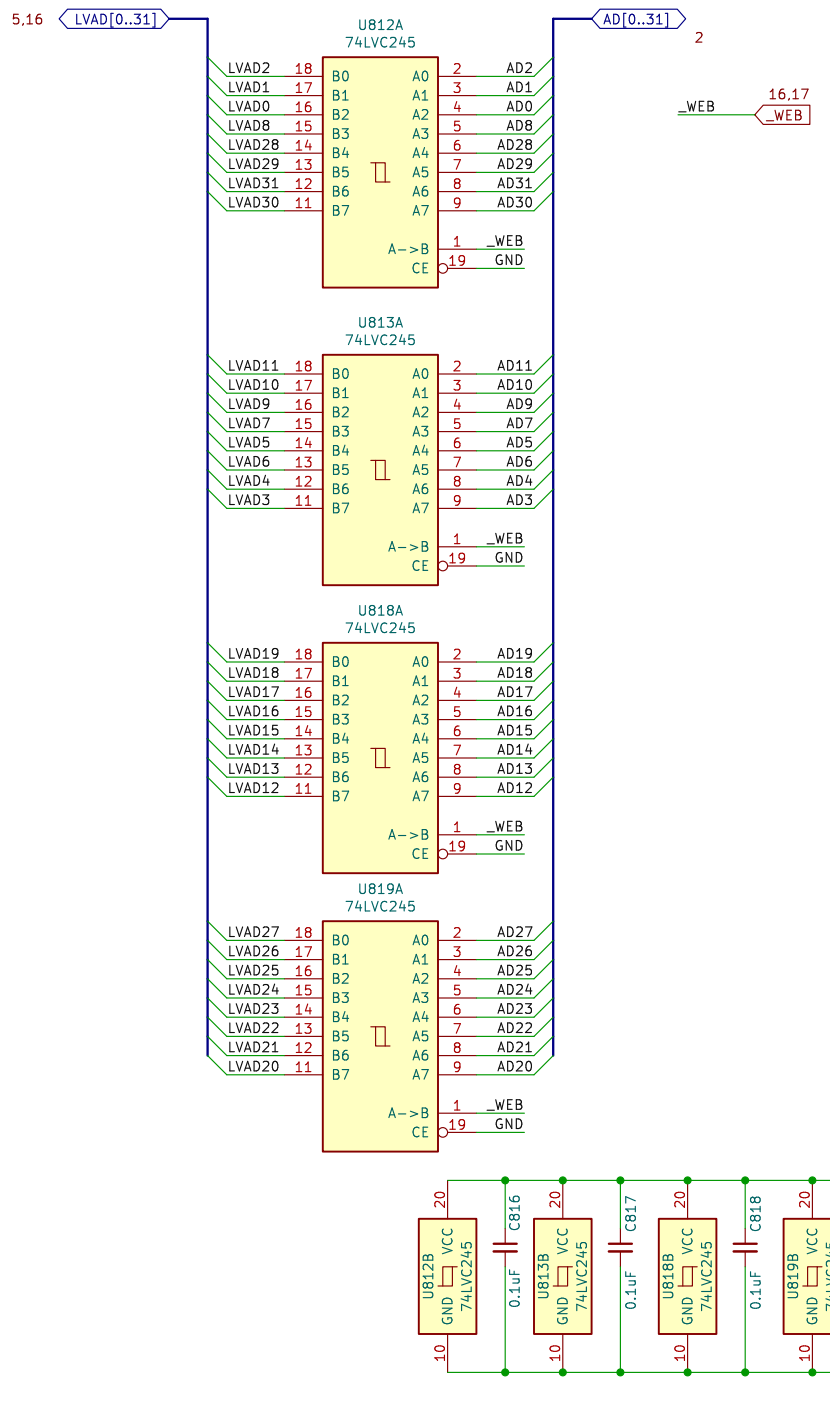


DO NOT USE COMMODORE 27C400 ROMS!

SYSTEM CLOCKS



AD BUS LEVEL SHIFT



CLOCKS AND ROMS

Sheet: /Clocks and PCI Buffers/
File: clocks.kicad_sch

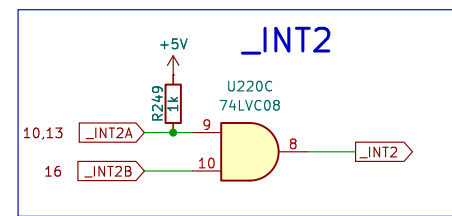
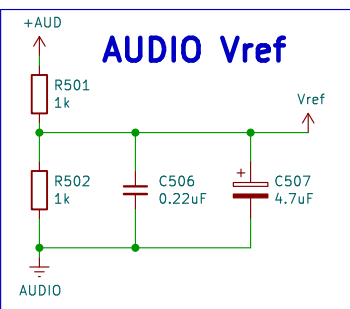
Title: AMIGA PCI

Size: B Date: 2025-01-11

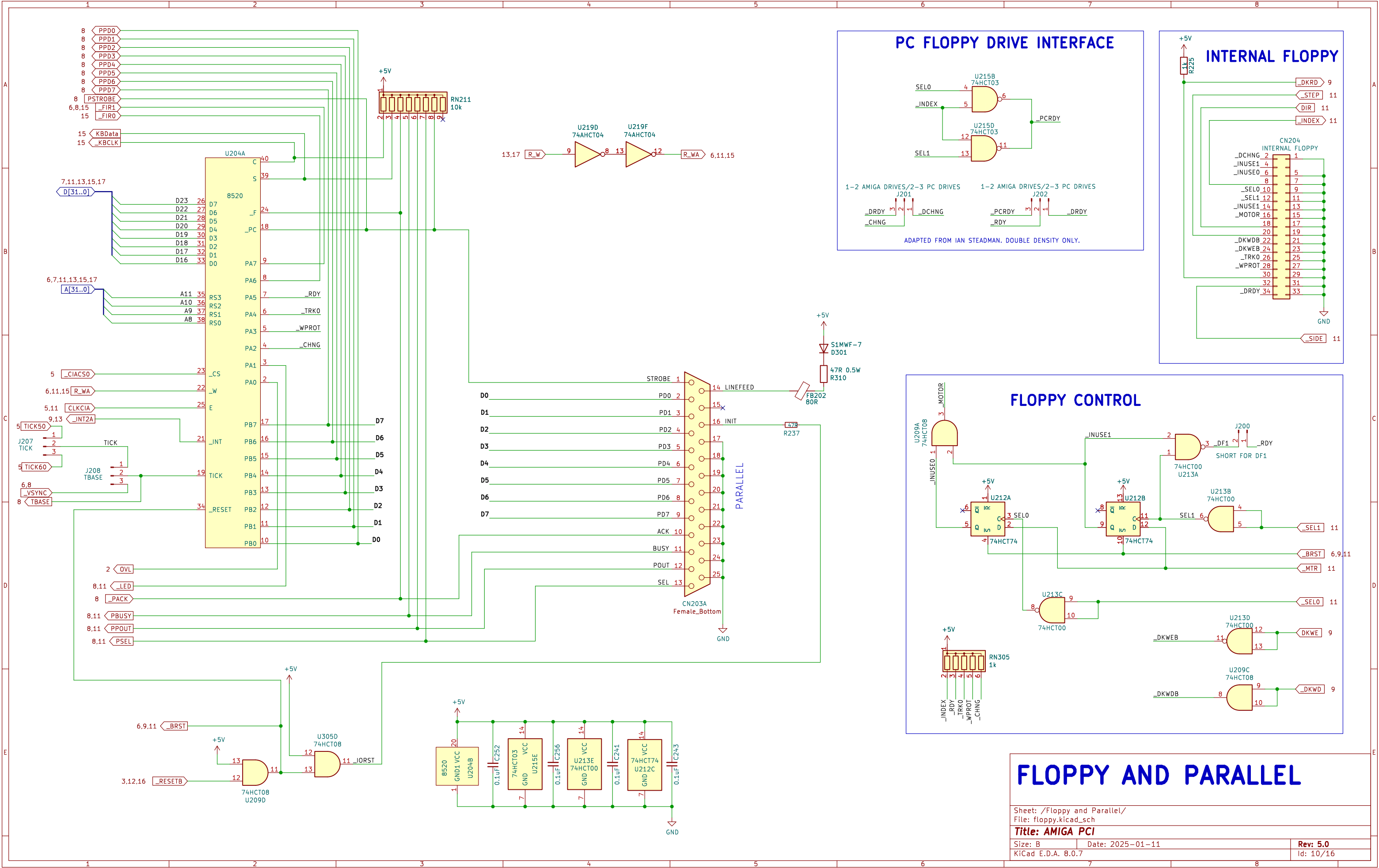
KiCad E.D.A. 8.0.7

Rev: 5.0

Id: 7/16



Id: 9/16



A

B

C

D



5



(



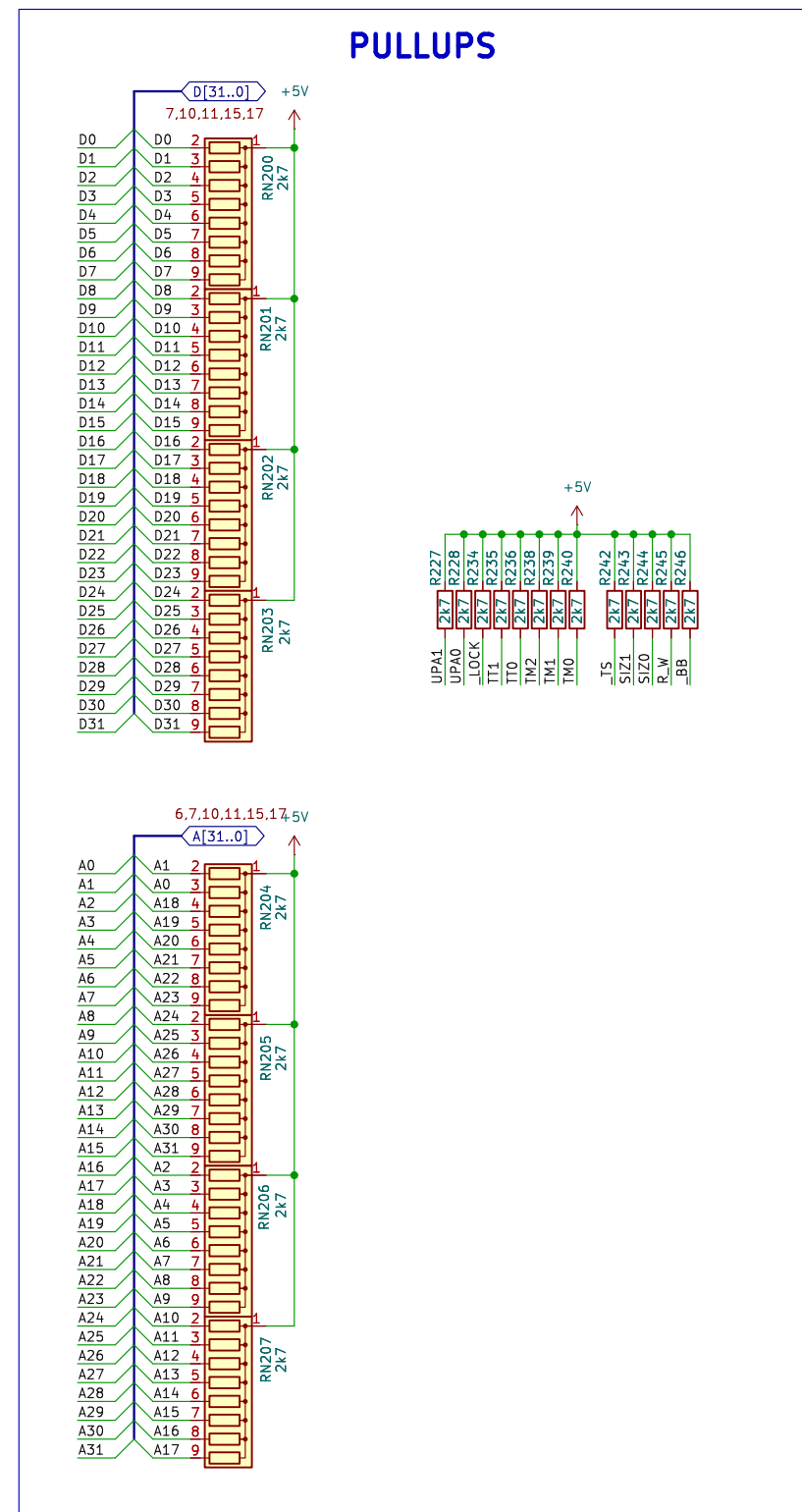
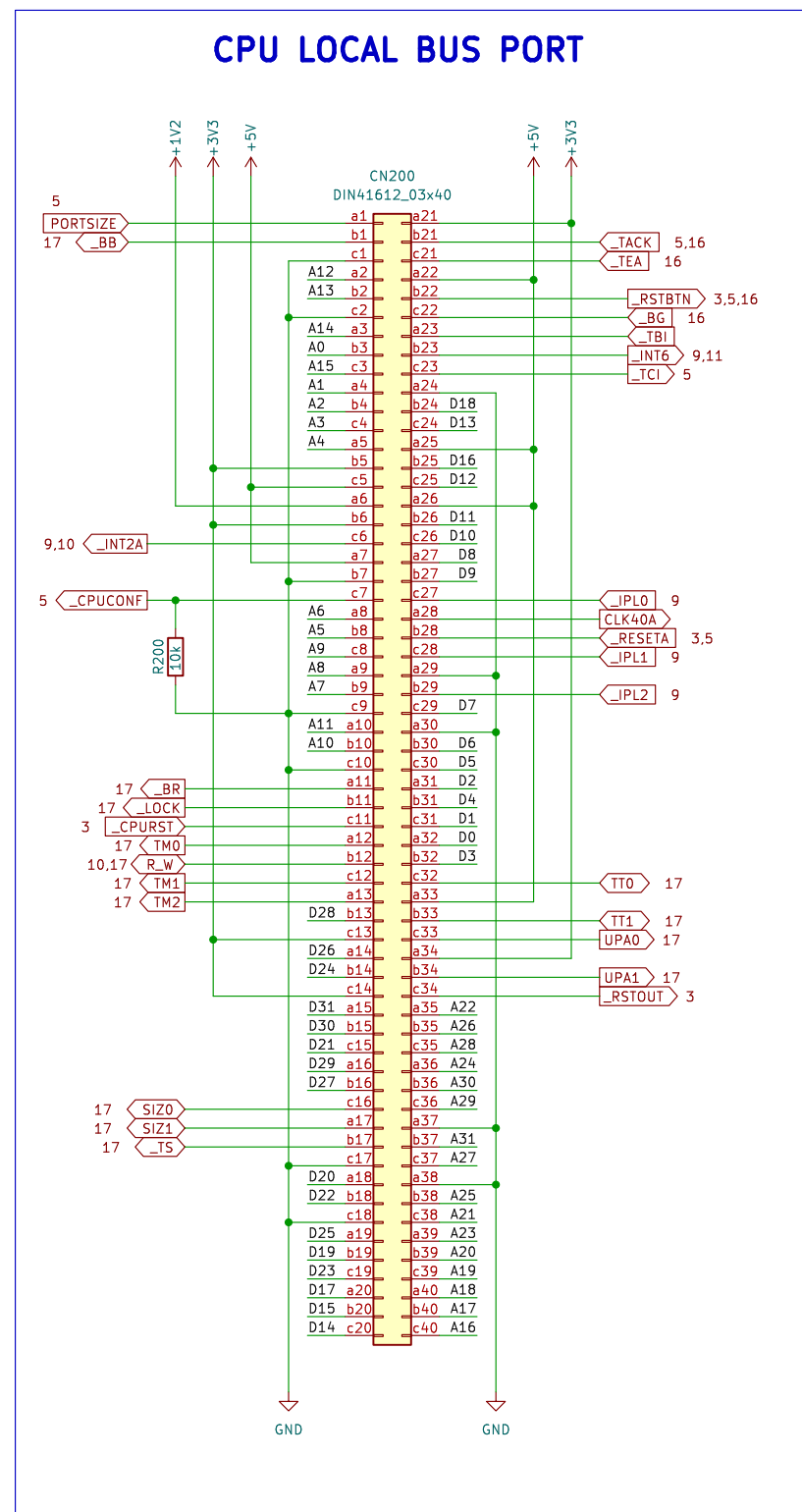
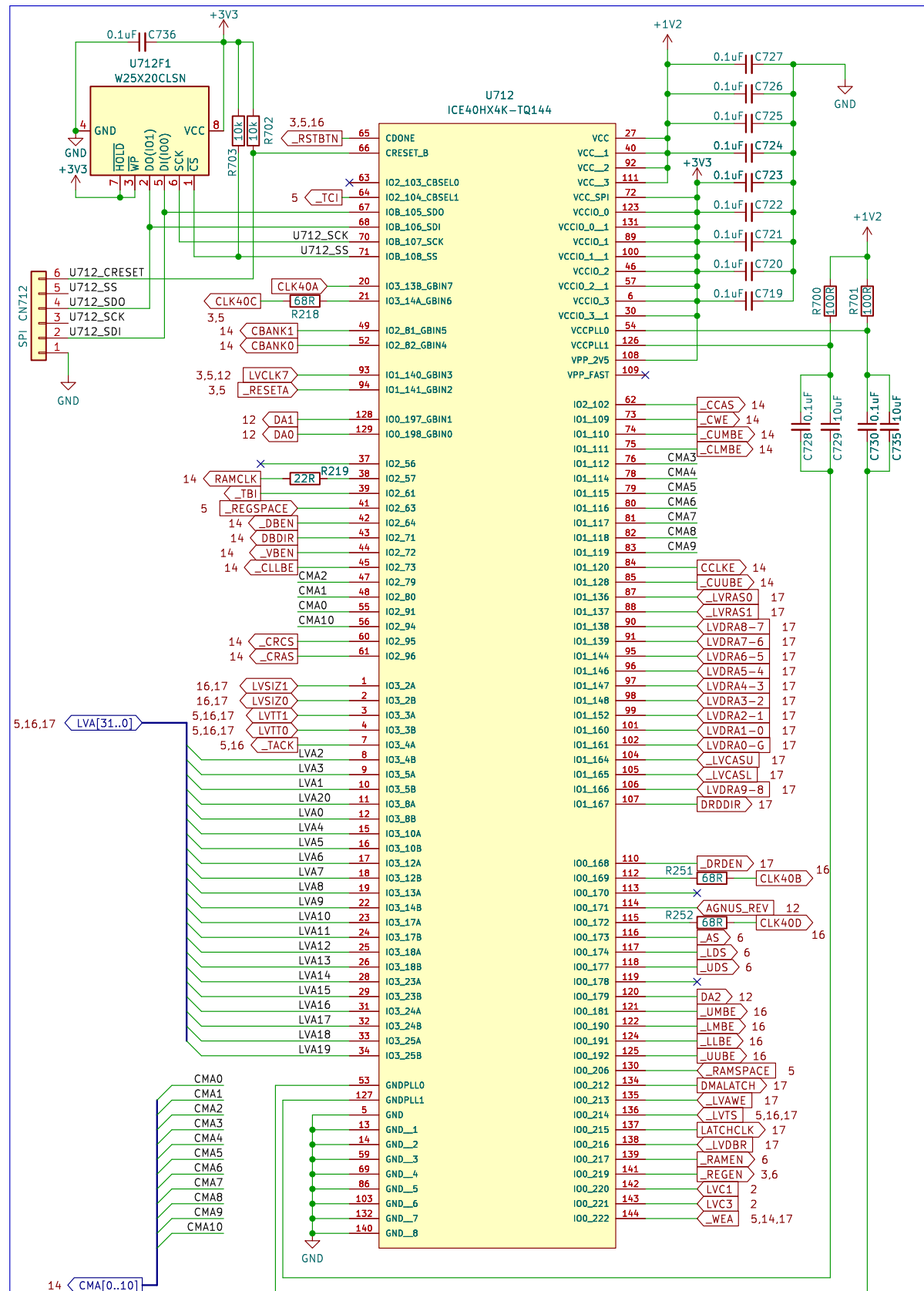
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Title: AMIGA PCI

Size: B	Da
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Date: 2025-01-11

d: 12/16



CHIP RAM CONTROLLER

Sheet: /Chip RAM Controller/

File: cpu.kicad_sch

Title: AMIGA PCI

Size: B

Date: 2025-01-11

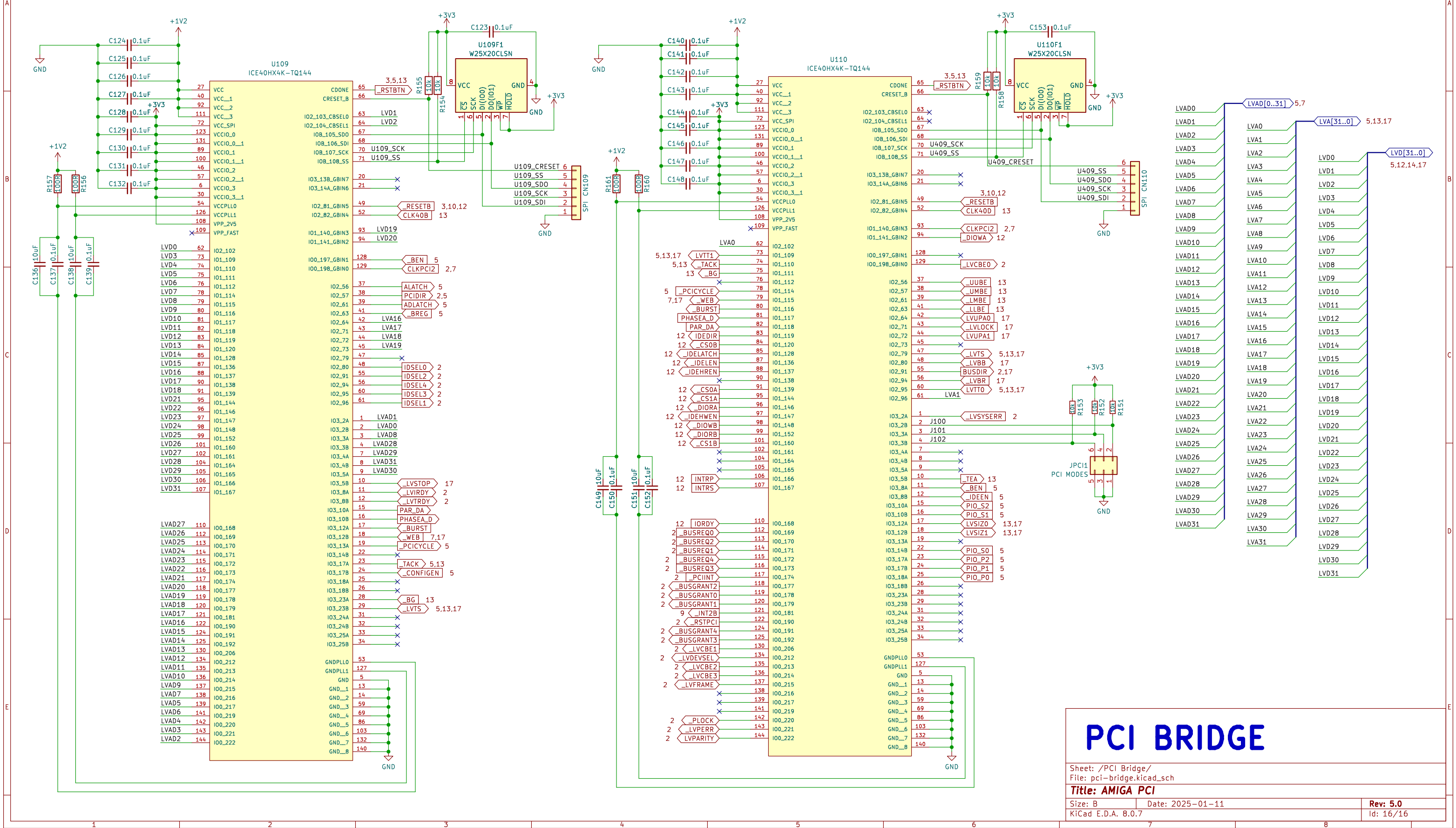
Rev: 5.0

Size: B
KiCad E.D.A. 8.0.7

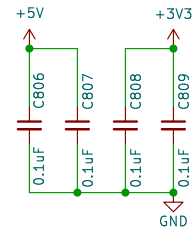
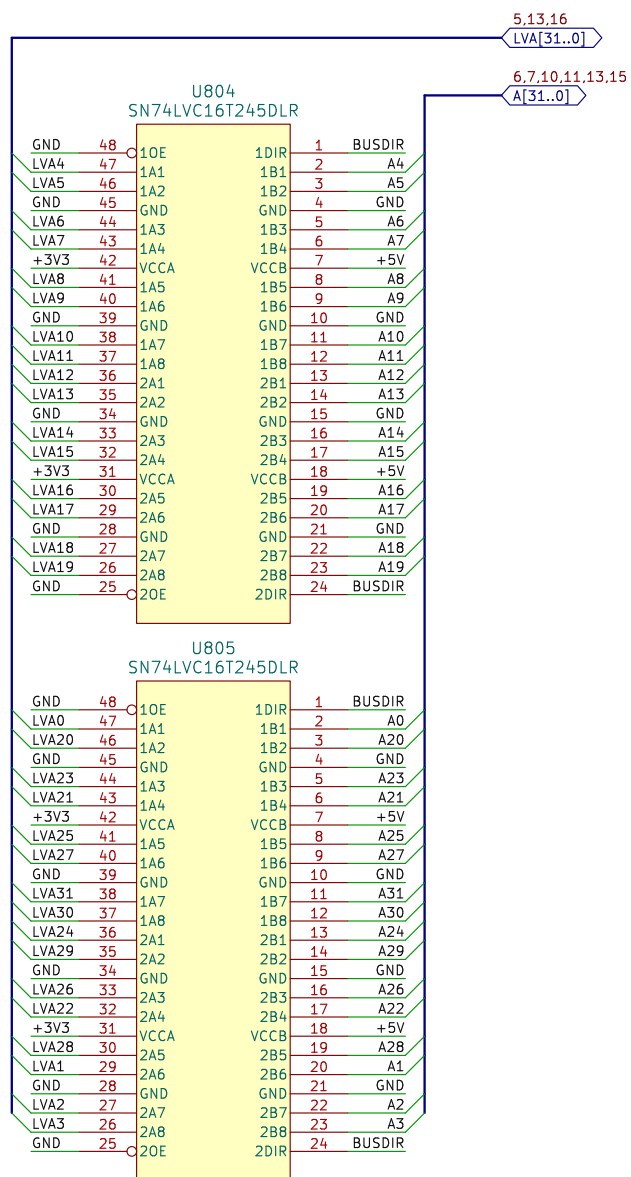
Id: 13/16

U109: BUFFERS AD/D/A BUSSES, AD PARITY CALC, BRIDGE REGISTERS

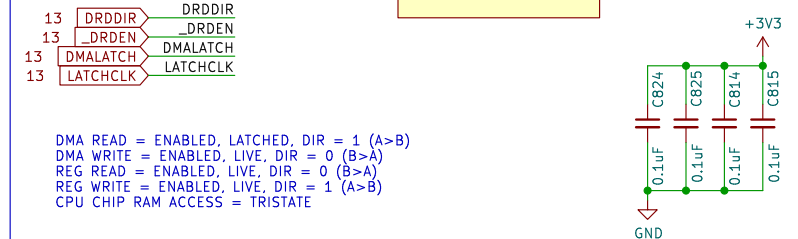
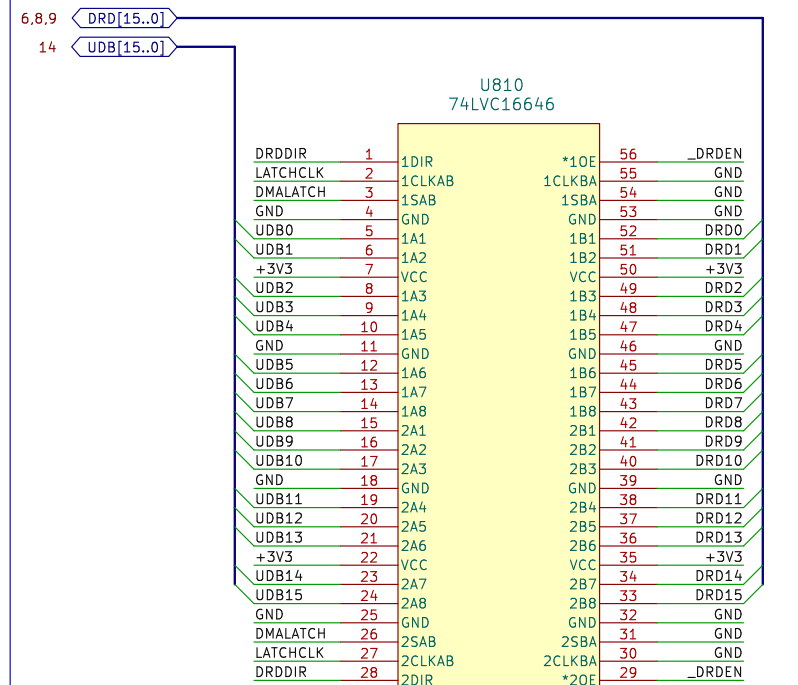
U110: BUS ARBITRATOR, INTERRUPT HANDLING, PARITY ERROR DETECTION, ATA CONTROLLER



LOW VOLTAGE ADDRESS BUFFERS

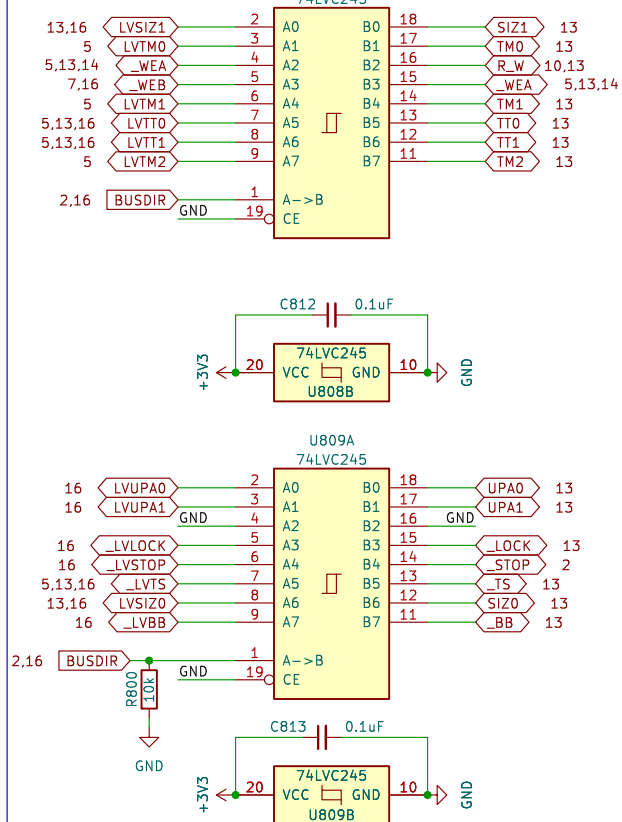


LOW VOLTAGE CHIPSET RAM DATA BUS BUFFER/LATCH



U080A
74LVC245

Pin	Signal	Pin	Signal	Pin	Signal
13,16	LVSIZ1	2	A0	18	SIZ1
5	LVTMO	3	A1	17	TMO
5,13,14	_WEA	4	A2	16	R_W
7,16	_WEB	5	A3	15	_WEA
5	LVTM1	6	A4	14	TM1
5,13,16	LVTTO	7	A5	13	TTO
5,13,16	LVTI1	8	A6	12	TI1
5	LVTM2	9	A7	11	TM2



MC68040 LEVEL SHIFT

Rev. 010	Id: 17/16
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